

Contents of Amendment under Article 34

SPECIFICATION

- 1) Page 5, line 2, after "comprises:" insert --a capacitance element one end of which is connected to a power supply voltage; --;
- 2) Page 5, line 4, change "a power supply voltage" to --an output of other end of the capacitance element--;
- 3) Page 5, line 8-11, delete "a capacitance element one end of which is connected to a power supply terminal which applies said power supply voltage and the other end of which is connected to the one side input node of the comparator;"
- 4) Page 5, line 14, before "reference" insert --inputted--;
- 5) Page 5, line 14, change "power supply voltage varies" to --inputted output of other end of the capacitance element occurs";
- 6) Page 5, line 20, before "reference" insert --inputted--;
- 7) Page 5, line 20, change "power supply voltage" to -- inputted output of other end of the capacitance element--";
- 8) Page 5, line 25, change "terminal" to --voltage--;
- 9) Page 6, line 2, delete "power supply";
- 10) Page 6, line 19, change "power supply voltage" to --output of other end of the capacitance element--;
- 11) Page 7, line 1, after "comprises:" insert --a first and a second capacitance elements one end of which is connected to

a power supply voltage;--;

12) Page 7, line 1, delete "and a second";

13) Page 7, line 1, change "comparators each of" to --
comparator--;

14) Page 7, line 3, change "a power supply voltage" to --an output
of other end of the first capacitance element--;

15) Page 7, line 5, after "result;" insert --a second comparator
which has two input nodes having opposite polarity to each other
and receives a reference voltage and an output of other end of
the second capacitance element at their inputs to compare the
respective voltage values to output a signal indicating a
comparison result;--;

16) Page 7, line 8-12, delete " a first and a second capacitance
elements, one end of which is both connected to a power supply
terminal which applies said power supply voltage, and the other
end of which is connected to the one side input node of the first
and the second comparator, respectively;"

17) Page 7, line 17, before "reference" insert --inputted--;

18) Page 7, line 17-18, change "power supply voltage varies"
to --inputted output of other end of the capacitance element
occurs--;

18) Page 7, line 19, change "power supply voltage" to --output
of other end of the first capacitance element--;

19) Page 7, line 20-21 change "power supply voltage" to --output
of other end of the second capacitance element--;

20) Page 7, line 25, before "being" insert --respectively--;

- 21) Page 8, line 2, before "reference" insert --inputted--;
- 22) Page 8, line 2, change "power supply voltage" to --inputted output of other end of the first capacitance element--;
- 23) Page 8, line 3-6, delete "Thereby, the variations in the power supply voltage which do not affect the operation of the semiconductor device may not be erroneously detected as abnormal voltage variation.";
- 24) Page 8, line 10, change "terminal" to --voltage--;
- 25) Page 8, line 12-13, delete "power supply";
- 26) Page 9, line 1, change "power supply voltage" to --output of other end of the first capacitance element--;
- 27) Page 9, line 2, change "and the second comaparators" to --comparator and the value of the output of other end of the second capacitance element which is inputted to either of the input nodes of the second comparator--;
- 28) Page 9, line 12, after "comprises:" insert --a capacitance element one end of which is connected to a power supply voltage; --;
- 29) Page 9, line 14, change "a power supply voltage" to --an output of other end of the capacitance element--;
- 30) Page 9, line 18-21, delete "a capacitance element one end of which is connected to a power supply terminal which applies said power supply voltage and the other end of which is connected to the one side input node of the comparator;"
- 31) Page 9, line 23, before "reference" insert --inputted--;
- 32) Page 9, line 24, change "power supply voltage varies" to

--inputted output of other end of the capacitance element occurs--;

33) Page 10, line 10, before "reference" insert --inputted--;

34) Page 10, line 10, change "power supply voltage" to --inputted output of other end of the capacitance element--;

35) Page 10, line 18, change "terminal" to --voltage--;

36) Page 10, line 20, delete "power supply";

37) Page 11, line 17, change "power supply voltage" to --output of other end of the capacitance element--;

38) Page 11, line 22, after "comprises:" insert --a first and a second capacitance elements one end of which is connected to a power supply voltage;--;

39) Page 11, line 22, delete "and a second";

40) Page 11, line 22, change "comparators each of" to --comparator--;

41) Page 11, line 24-25, change "a power supply voltage" to --an output of other end of the first capacitance element--;

42) Page 12, line 1, after "result;" insert --a second comparator which has two input nodes having opposite polarity to each other and receives a reference voltage and an output of other end of the second capacitance element at their inputs to compare the respective voltage values to output a signal indicating a comparison result;--;

43) Page 12, line 4-9, delete "a first and a second capacitance elements, one end of which is both connected to a power supply

terminal which applies said power supply voltage, and
respective the other end of which is connected to the one side
input node of the first and the second comparators,
respectively;";

44) Page 12, line 14, before "reference" insert --inputted--;

45) Page 12 line 14, change "power supply voltage varies" to
--inputted output of other end of the capacitance element
occurs--;

46) Page 12, line 15-16, change "power supply voltage" to -
-output of other end of the first capacitance element--;

47) Page 12 line 17, change "power supply voltage" to --output
of other end of the first capacitance element--;

48) Page 13, line 2, after "comparator" insert --
respctively--;

49) Page 13, line 4, before "reference" insert --inputted--;

50) Page 13, line 4-5, change "power supply voltage" to --
inputted output of other end of the first capacitance element--;

51) Page 13, line 15, change "terminal" to --voltage--;

52) Page 13, line 18, delete "power supply";

53) Page 14, line 13, change "power supply voltage" to --output
of other end of the first capacitance element--;

54) Page 14, line 14 change "and the second comparators" to
--comparator and the value of the output of other end of the
second capacitance element which is inputted to either of the
input nodes of the second comparator--;

55) Page 16, line 10, change "terminal" to --voltage--;

56) Page 16, line 18, delete "power supply";

57) Page 17, line 21-22, change ", and receives the reference voltage and the power supply voltage at its inputs and compares those." to --. The capacitor element 3 has its one side end which is connected to the power supply voltage 4 and its other side end which is connected to one side input terminal (input terminal N1) of the comparator 1 via the signal line L1. The input terminal 5 of the reference terminal is connected to the other side input terminal (input terminal N2) of the comparator 1 via the signal line L2. The comparator 1 receives the reference voltage and an output of the other end side of capacitance 3 to compare those.--;

58) Page 18, line 1-6, delete "The capacitor element 3 has its one side end which is connected to the power supply terminal 4 and its other side end which is connected to one side input terminal (input terminal N1) of the comparator 1. The input terminal 5 of the reference terminal is connected to the other side input terminal (input terminal N2) of the comparator 1 via the signal line L2.";

59) Page 18, line 22-23, change "a power supply voltage VDD is applied to the power supply voltage terminal 4" to --the power supply voltage 4 (the power supply voltage VDD) is applied--;

60) Page 18, line 24-25, delete "to the input terminal 5 of the reference voltage";

61) Page 20, line 2-4, change "the power supply voltage which was divided by the resister elements is simply compared with the reference voltage" to --simply the power supply voltage is divided by the resister element and the voltage which was divided by the resister elements is compared with the reference voltage--;

62) Page 20, line 9, change "the reference voltage and the power supply voltage value" to --the value of the output of other end side of the capacitance element 3 has one side which is connected to the power supply voltage and the reference voltage--;

63) Page 21, line 11, change "power supply voltage" to --output of the capacitance element 3--;

64) Page 21, line 18-19, change "a power supply voltage VDD is applied to the power supply terminal 4" to --the power supply voltage 4 (the power supply voltage VDD) is applied--;

65) Page 22, line 24-25, change "power supply voltage value" to --value of the output of the capacitance element 3--;

66) Page 24, line 14-15, change "and receives the reference voltage and the power supply voltage as at its inputs to compare those" to --. The capacitance element 3 has its one side end which is connected to the power supply voltage 4 and its other side end which is connected to one side input terminal (input terminal N3) of the hysteresis comparator 6.

The hysteresis comparator 6 receives the reference voltage and the output of the other side end of the capacitance element 3 to compare those--;

67) Page 24, line 17-18, change "and receives the reference voltage and the power supply voltage as at its inputs to compare those" to --. The capacitance element 9 has its one side end which is connected to the power supply voltage 4 and its other side end which is connected to one side input terminal (input terminal N5) of the hysteresis comparator 6. The hysteresis comparator 7 receives the reference voltage and the output of the other side end of the capacitance element 9 to compare those--;

68) Page 24, line 19, change "power supply voltage" to --output of the other side end of the capacitance element 9--;

69) Page 25, line 2-9 delete "The capacitance element 3 has its one side end which is connected to the power supply terminal 4 and its other side end which is connected to one side input terminal (input terminal N3) of the hysteresis comparator 6. The capacitance element 9 has its one side end which is connected to the power supply terminal 4 and its other side end which is connected to one side input terminal (input terminal N6) of the hysteresis comparator 7.";

70) Page 25, line 17-18, change "a power supply voltage VDD is applied to the power supply voltage terminal 4" to -- the power supply voltage 4 (a power supply voltage VDD) is applied--;

71) Page 26, line 13-14, change "the power supply voltage VDD is applied to the power supply terminal 4" to -- the power supply voltage 4 (the power supply voltage VDD) is applied--;

72) Page 27, line 8-9, change "power supply voltage value" to

--value of the output of the capacitance elements 3 and 9--;

73) Page 28, line 15, delete "power supply";

74) Page 28, line 22-23, change "a power supply voltage VDD is applied to the power supply terminal 4" to -- the power supply voltage 4 (the power supply voltage VDD) is applied--;

75) Page 29, line 18-19, change "the power supply voltage VDD is applied to the power supply voltage terminal 4" to -- the power supply voltage 4 (the power supply voltage VDD) is applied--;

76) Page 29, line 22, delete "power supply";

77) Page 30, line 4, change "power supply voltage" to --value of the output of the capacitance element 3--;

78) Page 30, line 12, "power supply voltage value" to --value of the output of the capacitance element 3--;

79) Page 31, line 24, delete "power supply";

80) Page 32, line 1, delete "power supply";

81) Page 32, line 22-23, change "a power supply voltage VDD is applied to the power supply terminal 4" to -- the power supply voltage 4 (the power supply voltage VDD) is applied--;

82) Page 33, line 22, change "power supply voltage VDD" to --output of the capacitance element 3--;

83) Page 34, line 23, change "power supply voltage" to --output of the capacitance element--;